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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/810,510	03/26/2004	Fan Ho	24295/81401	1180
7590 03/27/2006			EXAMINER	
Philip W. Wo	o FIN BROWN & WOOI	ANDUJAR, LEONARDO		
Suite 5000			ART UNIT	PAPER NUMBER
555 California	Street	2826		
San Francisco, CA 94104-1715			DATE MAILED: 03/27/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
و باند		Application No.				
	Office Action Commence	10/810,510	HO, FAN	And		
	Office Action Summary	Examiner	Art Unit	(h		
<u> </u>		Leonardo Andújar	2826			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
2a)	Responsive to communication(s) filed on <u>26</u> This action is FINAL . 2b) The Since this application is in condition for allow closed in accordance with the practice under	nis action is non-final. vance except for formal matters, pro		is		
Dispositi	ion of Claims					
5) [6) [7) [Claim(s) 1-31 is/are pending in the application 4a) Of the above claim(s) is/are withd Claim(s) is/are allowed. Claim(s) is/are rejected. Claim(s) is/are objected to. Claim(s) 1-31 are subject to restriction and/or	rawn from consideration.				
Applicati	ion Papers					
9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority (under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
2) Notice 3) Infor	nt(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) rmation Disclosure Statement(s) (PTO-1449 or PTO/SB/ er No(s)/Mail Date	4) Interview Summar Paper No(s)/Mail D 08) 5) Notice of Informal 6) Other:				

DETAILED ACTION

Election/Restriction

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1-20, drawn to a semiconductor device, classified in class 257, subclass 686.
 - II. Claims 24-31, drawn to a method of manufacturing a semiconductor device, classified in class 438, subclass 109.
 - III. Claims 21-23, drawn to a method of testing an integrated circuit chip, classified in class 341, subclass 120.

The inventions are distinct, each from the other because of the following reasons:

2. The inventions are distinct, each from the other because of the following reasons: Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case, unpatentability of the Group I invention would not necessarily imply unpatentability of the Group II invention, because the device of Group I invention could be made by a process materially different from that of the Group II invention. For example, the product of claim 1 can be made by another and materially different process such as a process that respectively couple the first/second IC chips with first/second ground planes whereas the ground planes are provided *in* the substrate. Note that method of claim 24 specifies that the ground planes are provided

on the substrate. Also, the process of claim 24 can be use to make a multi chip devices where the chips are provided within a substrate. Note that claim 1 specifies that the fist/second IC chips are provided on the substrate whereas claim 24 does not specify the chip locations.

- 3. Inventions III and I are related as product and process of use. The inventions can be shown to be distinct if either or both of the following can be shown: (1) the process for using the product as claimed can be practiced with another materially different product or (2) the product as claimed can be used in a materially different process of using that product. See MPEP § 806.05(h). In the instant case, product of claim 1 can be tested by testing the first chip while affecting an operation of the second IC chip..
- 4. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, the fields of search are not co-extensive and separate examination would be require, restriction for examination purposes as indicated is proper.
- 5. Applicant is advised that the response to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).
- 6. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by

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a diligently-filed petition under 37 CFR 1.48(b) and by the fee required under 37 CFR

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1.17(h).

7. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Leonardo Andújar whose telephone number is 571-272-

1912. The examiner can normally be reached on Mon through Thu from 9:00 AM to

7:30 PM EST.

8. If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Nathan J. Flynn can be reached on 571-272-1915. The fax phone number

for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published

applications may be obtained from either Private PAIR or Public PAIR. Status

information for unpublished applications is available through Private PAIR only. For

more information about the PAIR system, see http://pair-direct.uspto.gov. Should you

have guestions on access to the Private PAIR system, contact the Electronic Business

Center (EBC) at 866-217-9197 (toll-free).

Primary Examine

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